

# Meeting Minutes

2/16/15

Confirm midterm demo date  
end of Feb.

- Hardware
- Software
- Demo

• Location: In Lab

Sampling rate:

- Set target rate.
- pause between samples
- 10 a second  
- possibly faster

Scores not graded yet for powerpoint

Ethics Quiz: Due in March

will be here till  
5:30 pm

Lecture this week

## Testing:

ADC pins



Control Logic

CS/SHDN

Din

CLOCK

Shift Register

Dout

MCP3008 ADC

\*problems with permissions

p2 → VDD

P18 → Din

p2 → CLOCK

p22 → CS/SHDN

P16 → Dout

• 2

• 12

• 16

• 18

• 22

- oscilloscope reading 51.00 MHz  
Should it be GHz?
- Need to make aluminum board to reflect signal
- Had to reconfigure pins in the code for the Gv1
- Reading from current folder
- Had to change permissions in the code  
Chmod 777
- Fix syntax bugs
- Reset pin status
- Update tables in relative directories
- Set permissions
- Modify virtual host files
- Raspberry pi is extremely slow
- minimum sampling size is 10 seconds
- possibly replace pi with new one and faster processor.  
Revision number to check how old the pi is